

**What Is Claimed Is:**

- 1           1.       A method that predicts a result produced by a section of code in  
2       order to support speculative program execution, the section of code including a  
3       plurality of program instructions, the method comprising:  
4           executing the section of code within a program using a head thread,  
5       wherein executing the section of code produces the result;  
6           before the head thread produces the result, generating a predicted result to  
7       be used in place of the result;  
8           allowing the speculative thread to speculatively execute subsequent code  
9       within the program using the predicted result, wherein the subsequent code  
10      follows the section of code in an execution stream of the program; and  
11          after the head thread finishes executing the section of code, determining if  
12      a difference between the predicted result and the result generated by the head  
13      thread affected execution of the speculative thread;  
14          if the difference affected execution of the speculative thread, executing the  
15      subsequent code again using the result generated by the head thread; and  
16          if the difference did not affect execution of the speculative thread,  
17      performing a join operation to merge state associated with the speculative thread  
18      with state associated with the head thread.
- 1           2.       The method of claim 1, wherein executing the subsequent code  
2       again involves performing a rollback operation for the speculative thread to undo  
3       actions performed by the speculative thread.

1           3.       The method of claim 1, wherein determining if the difference  
2 affected execution of the speculative thread involves determining if the  
3 speculative thread accessed the predicted result.

1           4.       The method of claim 1, wherein determining if the difference  
2 affected execution of the speculative thread involves determining if the predicted  
3 result differs from the result generated by the head thread.

1           5.       The method of claim 1, wherein generating the predicted result  
2 involves looking up a value based upon a program counter for the program.

1           6.       The method of claim 5, wherein generating the predicted result  
2 involves additionally looking up the value based upon at least one previously  
3 generated value for the result.

1           7.       The method of claim 5, wherein generating the predicted result  
2 involves performing a function on the value.

1           8.       The method of claim 1, wherein executing the section of code  
2 involves performing one of:  
3           a method invocation to execute the section of code;  
4           a function call to execute the section of code; and  
5           a procedure call to execute the section of code.

1           9.       The method of claim 1, wherein the section of code is a body of a  
2 loop in the program, and the result is a loop carried dependency for the loop.

1           10.     The method of claim 1, wherein during a write operation to a  
2     memory element by the head thread, the method further comprises:  
3           performing the write operation to a primary version of the memory  
4     element;  
5           checking status information associated with the memory element to  
6     determine if the memory element has been read by the speculative thread;  
7           if the memory element has been read by the speculative thread, causing the  
8     speculative thread to roll back so that the speculative thread can read a result of  
9     the write operation; and  
10          if the memory element has not been read by the speculative thread,  
11     performing the write operation to a space-time dimensioned version of the  
12     memory element if the space-time dimensioned version exists.

1           11.     The method of claim 10, wherein performing the join operation  
2     involves merging the space-time dimensioned version of the memory element into  
3     the primary version of the memory element and discarding the space-time  
4     dimensioned version of the memory element.

1           12.     An apparatus that facilitates predicting a result produced by a  
2     section of code in order to support speculative program execution, the section of  
3     code including a plurality of program instructions, the apparatus comprising:  
4           a head thread that is configured to execute the section of code within a  
5     program, wherein executing the section of code produces the result;  
6           a prediction mechanism that is configured to generate a predicted result to  
7     be used in place of the result before the head thread produces the result;

8 a speculative thread that is configured to speculatively execute subsequent  
9 code within the program using the predicted result, wherein the subsequent code  
10 follows the section of code in an execution stream of the program; and  
11 a determination mechanism that is configured to determine if a difference  
12 between the predicted result and the result generated by the head thread affected  
13 execution of the speculative thread; and  
14 a joining mechanism that is configured to merge state associated with the  
15 speculative thread with state associated with the head thread if the difference did  
16 not affect execution of the speculative thread;  
17 wherein if the difference affected execution of the speculative thread, the  
18 apparatus is configured to execute the subsequent code again using the result  
19 generated by the head thread.

1 13. The apparatus of claim 12, wherein while executing the subsequent  
2 code again, the apparatus is configured to perform a rollback operation for the  
3 speculative thread to undo actions performed by the speculative thread.

1 14. The apparatus of claim 12, wherein the determination mechanism  
2 is configured to determine if the speculative thread accessed the predicted result.

1 15. The apparatus of claim 12, wherein the determination mechanism  
2 is configured to determine if the predicted result differs from the result generated  
3 by the head thread.

1 16. The apparatus of claim 12, wherein the prediction mechanism is  
2 configured to generate the predicted result by looking up a value based upon a  
3 program counter for the program.

1           17.     The apparatus of claim 16, wherein the prediction mechanism is  
2     configured to generate the predicted result by additionally looking up the value  
3     based upon at least one previously generated value for the result.

1           18.     The apparatus of claim 16, wherein the prediction mechanism is  
2     configured to generate the predicted result by performing a function on the value.

1           19.     The apparatus of claim 12, wherein the section of code includes  
2     one of, a method, a function and a procedure.

1           20.     The apparatus of claim 12, wherein the section of code is a body of  
2     a loop in the program, and the result is a loop carried dependency for the loop.

1           21.     The apparatus of claim 12, further comprising a mechanism that  
2     performs write operations for the head thread, the mechanism being configured to:  
3             perform a write operation to a primary version of a memory element;  
4             check status information associated with the memory element to determine  
5     if the memory element has been read by the speculative thread;  
6             cause the speculative thread to roll back so that the speculative thread can  
7     read a result of the write operation if the memory element has been read by the  
8     speculative thread; and  
9             perform the write operation to a space-time dimensioned version of the  
10    memory element if the space-time dimensioned version exists and if the memory  
11    element has not been read by the speculative thread.

1           22.     The apparatus of claim 21, wherein the joining mechanism is  
2 configured to:  
3           merge the space-time dimensioned version of the memory element into the  
4 primary version of the memory element; and to  
5           discard the space-time dimensioned version of the memory element.

1           23.     A computer-readable storage medium storing instructions that  
2 when executed by a computer cause the computer to perform a method that  
3 predicts a result produced by a section of code in order to support speculative  
4 program execution, the section of code including a plurality of program  
5 instructions, the method comprising:  
6           executing the section of code within a program using a head thread,  
7 wherein executing the section of code produces the result;  
8           before the head thread produces the result, generating a predicted result to  
9 be used in place of the result;  
10          allowing the speculative thread to speculatively execute subsequent code  
11 within the program using the predicted result, wherein the subsequent code  
12 follows the section of code in an execution stream of the program; and  
13          after the head thread finishes executing the section of code, determining if  
14 a difference between the predicted result and the result generated by the head  
15 thread affected execution of the speculative thread;  
16          if the difference affected execution of the speculative thread, executing the  
17 subsequent code again using the result generated by the head thread; and  
18          if the difference did not affect execution of the speculative thread,  
19 performing a join operation to merge state associated with the speculative thread  
20 with state associated with the head thread.

1           24.     The computer-readable storage medium of claim 23, wherein  
2     executing the subsequent code again involves performing a rollback operation for  
3     the speculative thread to undo actions performed by the speculative thread.